

10/542576

JC17 Rec'd PCT/PTO 19 JUL 2005

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q89211

Shinichi HORI

Appln. No.: Not Assigned

Confirmation No.: Not Assigned

Group Art Unit: Not Assigned

Filed: July 19, 2005

Examiner: Not Assigned

For: GAIN-VARIABLE VOLTAGE-CURRENT CONVERSION CIRCUIT AND FILTERING
CIRCUIT INCLUDING THE SAME

INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. §§ 1.97 and 1.98

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure under 37 C.F.R. § 1.56, Applicant hereby notifies the U.S. Patent and Trademark Office of the documents which are listed on the attached PTO/SB/08 A & B (modified) form and/or listed herein and which the Examiner may deem material to patentability of the claims of the above-identified application.

1. Japanese Patent Application No. 1-233813, published September 19, 1989.
2. Japanese Patent Application No. 6-342561, published December 13, 1994.
3. Japanese Patent Application No. 42-14747, published August 17, 1967.
4. Japanese Patent Application No. 50-121643, published March 29, 1949.
5. Japanese Patent Application No. 10-242770, published September 11, 1998.
6. Japanese Patent Application No. 2000-68761, published March 3, 2000.

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7. Japanese Patent Application No. 3-64109, published March 19, 1991.
8. Japanese Patent Application No. 7-235840, published September 5, 1995.
9. Japanese Patent Application No. 2001-36356, published February 9, 2001.
10. F. Behbahani, et al. "A Broad-Band Tunable CMOS Channel-Select Filter for a Low-IF Wireless Receiver", IEEE, Journal of Solid-State Circuits, Vol. 35, No. 4, April 2000, pp 476-489.
11. B. Pankiewicz, et al. "A Field Programmable Analog Array for CMOS Continuous-Time OTA-A Filter Applications", IEEE, Journal of Solid-State Circuits, Vol. 37, No. 2, February 2002, pp 125-136.

One copy of each of the listed documents is submitted herewith.

The present Information Disclosure Statement is being filed: (1) No later than three months from the application's filing date; (2) Before the mailing date of the first Office Action on the merits (whichever is later); or (3) Before the mailing date of the first Office Action after filing a request for continued examination (RCE) under §1.114, and therefore, no Statement under 37 C.F.R. § 1.97(e) or fee under 37 C.F.R. § 1.17(p) is required.

In compliance with the concise explanation requirement under 37 C.F.R. § 1.98(a)(3) for foreign language documents, Applicant encloses herewith a copy of a Communication from the Japan Patent Office citing references 1-6 listed above.

The submission of the listed documents is not intended as an admission that any such document constitutes prior art against the claims of the present application. Applicant does not

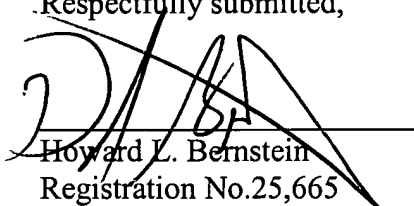
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waive any right to take any action that would be appropriate to antedate or otherwise remove any listed document as a competent reference against the claims of the present application.

The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account. A duplicate copy of this paper is attached.

Respectfully submitted,


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WASHINGTON OFFICE

23373

CUSTOMER NUMBER

Date: July 19, 2005

Substitute for Form 1449 A & B/PTO <u>INFORMATION DISCLOSURE</u> <u>STATEMENT BY APPLICANT</u> (use as many sheets as necessary)				<i>Complete if Known</i>		
				Application Number	10/542576	
				Confirmation Number		
				Filing Date	July 19, 2005	
				First Named Inventor	Shinichi HORI	
				Art Unit		
Examiner Name						
Attorney Docket Number	Q89211					
Sheet	1	of	1			

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
		Number	Kind Code ² (if known)		
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			
		US			

FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. ¹	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Translation ⁶
		Country Code ³	Number ⁴	Kind Code ⁵ (if known)			
		JP	1-233813	A	09-19-1989		
		JP	6-342561	A	12-13-1994		
		JP	42-14747	B1	08-17-1967		
		JP	49-32100		10-04-1975		
		JP	10-242770	A	09-11-1998		
		JP	2000-68761	A	03-03-2000		
		JP	3-64109	A	03-19-1991		
		JP	7-235840	A	09-05-1995		
		JP	2001-36356	A	02-09-2001		

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city, and/or country where published.	Translation ⁶
		F. Behbahani, et al. "A Broad-Band Tunable CMOS Channel-Select Filter for a Low-IF Wireless Receiver", IEEE, Journal of Solid-State Circuits, Vol. 35, No. 4, April 2000, pp 476-489.	
		B. Pankiewicz, et al. "A Field Programmable Analog Array for CMOS Continuous-Time OTA-A Filter Applications", IEEE, Journal of Solid-State Circuits, Vol. 37, No. 2, February 2002, pp 125-136.	

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² See Kind Codes of USPTO Patent Documents at www.uspto.gov, MPEP 901.04 or in the comment box of this document. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST. 3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to indicate here if English language Translation is attached.